

# INTERNATIONAL SEARCH REPORT

Inte      onal Application No  
PC., JK2004/000837

## A. CLASSIFICATION OF SUBJECT MATTER

IPC 7    H02M3/07    H04R1/04    H04R3/00    H04R19/00

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7    H02M    G11C    H04R

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5 490 220 A (LOEPPERT ET AL) 6 February 1996 (1996-02-06) figures 5-7 column 1, line 9 - line 30 column 1, line 46 - line 54 column 2, line 37 - line 42 column 5, line 35 - line 45 claim 1	1-27
A	EP 0 562 738 A (HEWLETT-PACKARD COMPANY) 29 September 1993 (1993-09-29) figures 1-4 column 1, line 10 - line 22 column 2, line 4 - line 6 column 4, line 44 - line 49 column 4, line 57 - column 5, line 2 column 5, line 51 - column 6, line 7 column 6, line 56 - column 7, line 5  ----- -/--	1-27

☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

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\*P\* document published prior to the international filing date but later than the priority date claimed

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\*X\* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

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Date of the actual completion of the international search

24 February 2005

Date of mailing of the international search report

04/03/2005

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In International Application No  
PCT/JP2004/000837

## C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

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P, X	WO 2004/057909 A (SONIONMEMS A/S; MUELLENBORN, MATTHIAS; VAN HALTEREN, AART, ZEGER; SONI) 8 July 2004 (2004-07-08) figures 1a,1b page 1, line 5 - line 7 page 1, line 27 - line 29 page 2, line 12 - line 20 page 6, line 30 - page 7, line 4	1-27
A	DICKSON J F: "ON-CHIP HIGH-VOLTAGE GENERATION IN MNOS INTEGRATED CIRCUITS USING AN IMPROVED VOLTAGE MULTIPLIER TECHNIQUE" IEEE JOURNAL OF SOLID-STATE CIRCUITS, IEEE INC. NEW YORK, US, vol. SC-11, no. 3, June 1976 (1976-06), pages 374-378, XP009035607 ISSN: 0018-9200 the whole document	1-27
A	US 5 675 279 A (FUJIMOTO ET AL) 7 October 1997 (1997-10-07) the whole document	1-27
A	US 2003/133588 A1 (PEDERSEN MICHAEL) 17 July 2003 (2003-07-17) the whole document	1-27

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Information on patent family members

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